



Timing applications engineer (Ethernet synchronization, NTP and IEEE-1588)

Job description

Seven Solutions S.L is a leading company specialized on embedded system design, electronics and synchronization products. We are looking for three high specialized engineers, challenge lovers, good team players and fluent on different languages.

The three positions offered are in the domain of firmware/gateway developments. Please, in order to apply it is **MANDATORY** to attach a cover letter, all applications without cover letter will be rejected automatically.

* **Generic skills**

- 2 years of experience minimum
- Fluent English
- Excellent team player and intercommunication skills

* **Specific skills for the Timing SW engineer job:**

- Advance C/C++ programming
- Experience with Linux network stack (sockets, daemons, etc)
- Synchronization protocols NTP and PTP (IEEE-1588) protocols
- Experience with evaluation of Timing and frequency techniques (Phase noise, Time counter)

* **Additional desirable skills:**

- Firmware and peripheral controls
- Experience integrating FPGA IP cores
- Mobile networks (4G/5G) equipment and architectures

You can apply by sending your CV and Cover Letter to the following email address:
info@sevensols.com

Please, indicate the name of the offer in the mail subject.